

US-120US

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appln. No: 10/686,974
Applicants: Rakesh Batish et al.
Filed: October 16, 2003
Title: SYSTEM AND METHOD FOR REDUCING OR ELIMINATING
SEMICONDUCTOR DEVICE WIRE SWEEP
TC/A.U.: 2823
Examiner: William D. Coleman

SUBMISSION OF FORMAL DRAWINGS

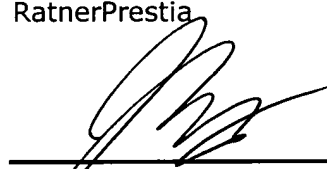
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Sir:

Submitted herewith are formal drawings for the above-identified application.

Respectfully submitted,

RatnerPrestia



Jacques L. Etkowicz, Reg. No. 41,738
Attorney for Applicants

JLE/kpc

Enclosures: (9) Sheets of Formal Drawings

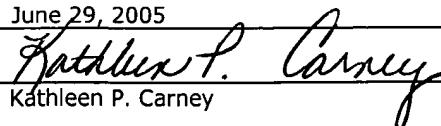
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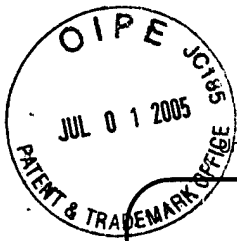
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Total Number of Pages in This Submission 14

Application Number	10/686,974
Filing Date	October 16, 2003
First Named Inventor	Rakesh Batish
Art Unit	2823
Examiner Name	William D. Coleman
Attorney Docket No.	K&S-120US

ENCLOSURES (Check all that apply)

- ☐ Fee Transmittal Form
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- ☐ Response to Missing Parts/
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under 37 CFR 1.52 or 1.53

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Firm Name	RatnerPrestia
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Printed Name	Jacques L. Etkowicz
Date	June 29, 2005

Registration No.	41,738
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